



# Domain Controller

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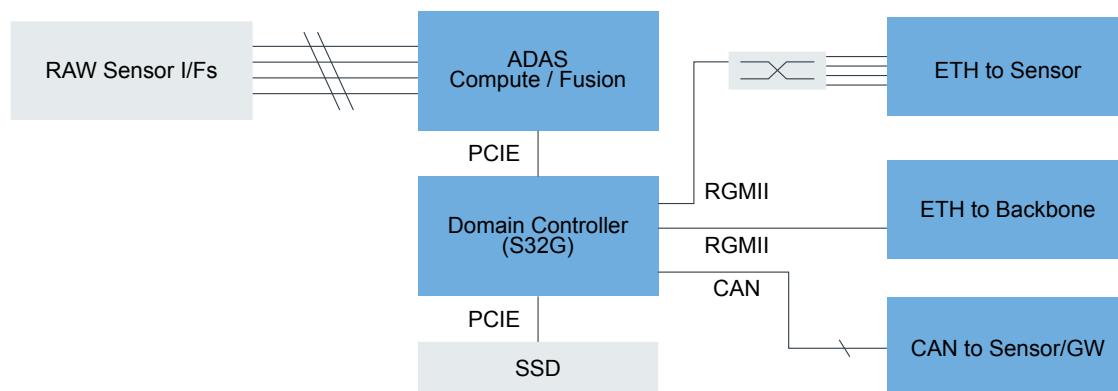
Domain control is the centralization of control of similar functions and functionality. Domain architectures aid in the reduction of vehicle engine control units (ECUs) and in the simplification of software development and distribution, as system complexity within the vehicle grows. The [NXP S32 automotive platform](#) captures the opportunity to move towards the transformation of vehicle electronic architectures (VEA) and the transition to the [software-defined vehicle](#) (SDV).

OEMs are approaching the transformation of the VEA by following two different vectors. The logical path is about coordination and consolidation of functional domains, including body domain, powertrain domain and ADAS domain. This is where domain controllers are required and where NXP provides optimized products and insights.

The other vector is the physical path, which looks to reduce the complexity of the wiring harness and thereby reduces vehicle weight and reduces costs. [Zone controllers](#), which control physical zones of the vehicle, improve the efficiency of energy consumption as well.

Both of these vectors can relate to each other, and OEMs are following architecture strategies that combine each of these vectors in varying ways. NXP provides optimized hardware and software to meet the needs of both vectors.

## ADAS Example Block Diagram



■ NXP Technology 
 ■ Non NXP Technology 
   Optional Technology

#### Recommended Products for ADAS Example

ADAS Compute / Fusion	<ul style="list-style-type: none"> <li>• <a href="#">LX2162A</a>: Layerscape® LX2162A, LX2122A, LX2082A Processors</li> <li>• <a href="#">FS5600</a>: Automotive Dual Buck Regulator and Controller with Voltage Monitors and Watchdog Timer</li> <li>• <a href="#">FS86</a>: Safety System Basis Chip For Domain Controller, Fit For ASIL B and D</li> <li>• <a href="#">LX2160A</a>: Layerscape® LX2160A, LX2120A, LX2080A Processors</li> </ul>
Domain Controller	<ul style="list-style-type: none"> <li>• <a href="#">S32G2</a>: S32G2 Processors for Vehicle Networking</li> <li>• <a href="#">S32G3</a>: S32G3 Processors for Vehicle Networking</li> <li>• <a href="#">PF53</a>: 12 A / 8 A / 15 A Core Supply Regulator with AVP and Watchdog</li> <li>• <a href="#">VR5510</a>: Multi-Channel (9) PMIC for S32G Processor – 8 High Power, 1 Low Power, Fit for ASIL D Safety Level</li> </ul>
Ethernet	<ul style="list-style-type: none"> <li>• <a href="#">TJA1103</a>: TJA1103, ASIL B Compliant Automotive Ethernet 100BASE-T1 PHY Transceiver</li> <li>• <a href="#">TJA1104</a>: TJA1104, MACsec Enabled ASIL B Compliant Automotive Ethernet 100BASE-T1 PHY Transceiver</li> <li>• <a href="#">TJA1120</a>: TJA1120, ASIL B Compliant Automotive Ethernet 1000BASE-T1 PHY Transceiver</li> </ul>

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CAN	<ul style="list-style-type: none"><li>• <a href="#">TJA1463</a>: CAN Signal Improvement Capability Transceiver with Sleep Mode</li><li>• <a href="#">TJA1462</a>: CAN Signal Improvement Capability Transceiver with Standby Mode</li></ul>

Propulsion Block Diagram

Propulsion Domain Control Variations

<p>Realtime EV</p> <p>PDC</p> <p>Communication</p> <p>ACDC</p> <p>DCDC</p> <p>INV</p> <p>BMS</p> <p>Chassis</p> <p>Brake</p> <p>Steer</p> <p>ICE</p>	<p>Full EV</p> <p>PCD</p> <p>ACDC</p> <p>DCDC</p> <p>INV</p> <p>BMS</p> <p>Chassis</p> <p>Brake</p> <p>Steer</p> <p>ICE</p>	<p>Dynamics</p> <p>PCD</p> <p>ACDC</p> <p>DCDC</p> <p>INV</p> <p>BMS</p> <p>Chassis</p> <p>Brake</p> <p>Steer</p> <p>ICE</p>	<p>Inverter +</p> <p>PDC</p> <p>ACDC</p> <p>DCDC</p> <p>INV</p> <p>BMS</p> <p>Chassis</p> <p>Brake</p> <p>Steer</p> <p>ICE</p>	<p>EV Control</p> <p>PCD</p> <p>ACDC</p> <p>DCDC</p> <p>INV</p> <p>BMS</p> <p>Chassis</p> <p>Brake</p> <p>Steer</p> <p>ICE</p>	<p>Realtime Chassis</p> <p>PDC</p> <p>ACDC</p> <p>DCDC</p> <p>INV</p> <p>BMS</p> <p>Chassis</p> <p>Brake</p> <p>Steer</p> <p>ICE</p>
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■ NXP Technology

■ Non NXP Technology

□ Optional Technology

Recommended Products for Propulsion

Realtime EV	<ul style="list-style-type: none"><li>• <a href="#">S32Z2</a>: S32Z2 Safe and Secure High-Performance Real-Time Processors</li></ul>
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	<ul style="list-style-type: none"> <li>• <a href="#">S32E2</a>: S32E2 Safe and Secure High-Performance Real-Time Processors with Actuation Support</li> <li>• <a href="#">FS86</a>: Safety System Basis Chip For Domain Controller, Fit For ASIL B and D</li> <li>• <a href="#">PF5030</a>: Multi-Channel PMIC for Automotive Applications</li> </ul>
Full EV	<ul style="list-style-type: none"> <li>• <a href="#">S32Z2</a>: S32Z2 Safe and Secure High-Performance Real-Time Processors</li> <li>• <a href="#">S32E2</a>: S32E2 Safe and Secure High-Performance Real-Time Processors with Actuation Support</li> <li>• <a href="#">FS86</a>: Safety System Basis Chip For Domain Controller, Fit For ASIL B and D</li> <li>• <a href="#">PF5030</a>: Multi-Channel PMIC for Automotive Applications</li> </ul>
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Inverter +	<ul style="list-style-type: none"> <li>• <a href="#">S32K39-37</a>: S32K39/37/36 Microcontrollers for Electrification Applications</li> <li>• <a href="#">FS26</a>: Safety System Basis Chip with Low Power, for ASIL D Systems</li> </ul>
EV Control	<ul style="list-style-type: none"> <li>• <a href="#">S32Z2</a>: S32Z2 Safe and Secure High-Performance Real-Time Processors</li> <li>• <a href="#">S32E2</a>: S32E2 Safe and Secure High-Performance Real-Time Processors with Actuation Support</li> <li>• <a href="#">PF5030</a>: Multi-Channel PMIC for Automotive Applications</li> <li>• <a href="#">FS86</a>: Safety System Basis Chip For Domain Controller, Fit For ASIL B and D</li> </ul>
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Realtime Chassis	<ul style="list-style-type: none"> <li>• <a href="#">S32E2</a>: S32E2 Safe and Secure High-Performance Real-Time Processors with Actuation Support</li> <li>• <a href="#">S32Z2</a>: S32Z2 Safe and Secure High-Performance Real-Time Processors</li> <li>• <a href="#">PF5030</a>: Multi-Channel PMIC for Automotive Applications</li> <li>• <a href="#">FS86</a>: Safety System Basis Chip For Domain Controller, Fit For ASIL B and D</li> </ul>
Communication	<ul style="list-style-type: none"> <li>• <a href="#">TJA1463</a>: CAN Signal Improvement Capability Transceiver with Sleep Mode</li> <li>• <a href="#">TJA1462</a>: CAN Signal Improvement Capability Transceiver with Standby Mode</li> <li>• <a href="#">TJA1103</a>: TJA1103, ASIL B Compliant Automotive Ethernet 100BASE-T1 PHY Transceiver</li> <li>• <a href="#">TJA1104</a>: TJA1104, MACsec Enabled ASIL B Compliant Automotive Ethernet 100BASE-T1 PHY Transceiver</li> <li>• <a href="#">TJA1120</a>: TJA1120, ASIL B Compliant Automotive Ethernet 1000BASE-T1 PHY Transceiver</li> </ul>

View our complete solution for [Domain Controller](#).

**Note:** The information on this document is subject to change without notice.

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